

FIG. 1 (Prior Art)

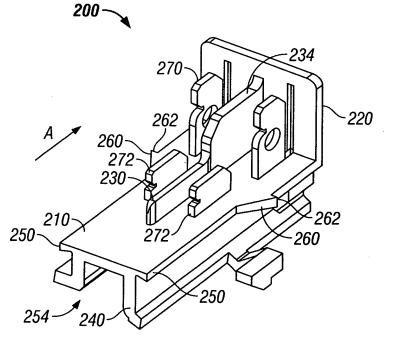


FIG. 2 (Prior Art)

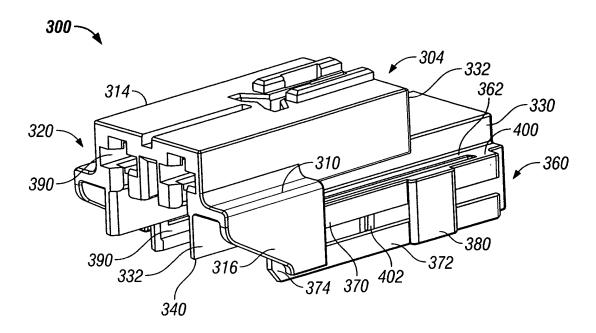


FIG. 3

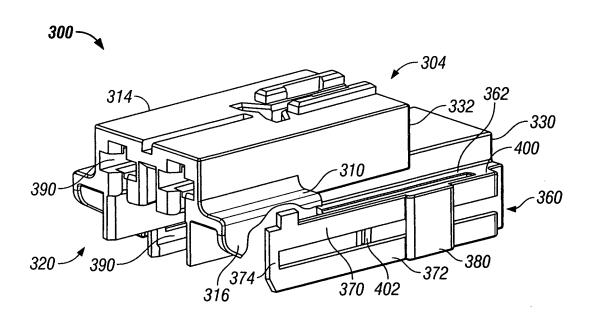


FIG. 4

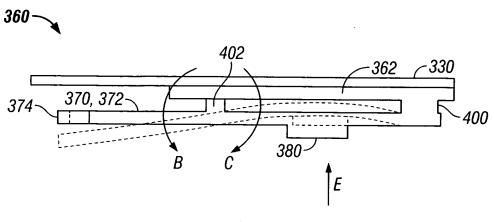


FIG. 5

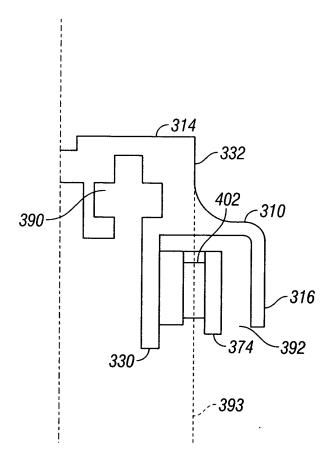


FIG. 6

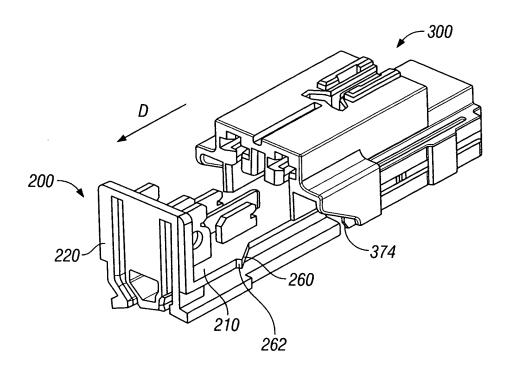


FIG. 7

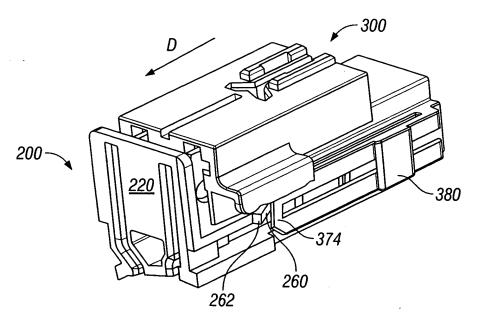


FIG. 8

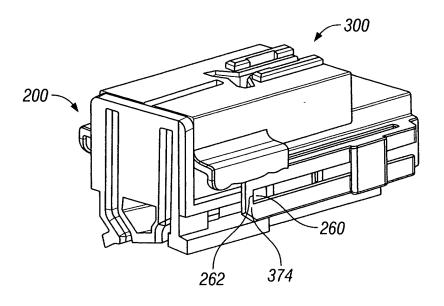


FIG. 9

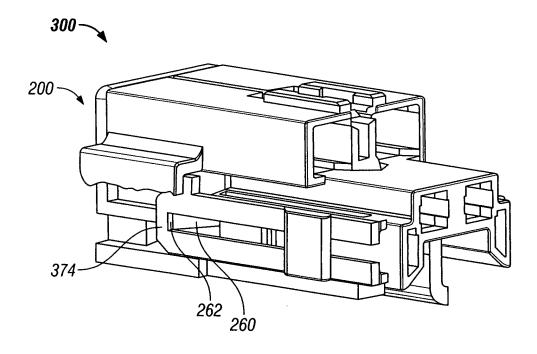


FIG. 10

1-22-00

OTPE JAN 2 1 2004 W

Docket 18002 (AT 20958-35) PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jeremy Christin Patterson

Serial No.: 10/705,790

Filed: November 10, 2003

11001 110 1011001 10, 200

For: ELECTRICAL CONNECTOR

WITH A LOW PROFILE LATCH

Examiner:

Art Unit:

TRANSMITTAL OF FORMAL DRAWINGS

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Sir:

Enclosed are five (5) sheets of formal drawings for the above-identified patent

application. No new matter has been added.

Date: January 21, 2004

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